SYSTOLIC VLSI ARRAYS FOR POLYNOMIAL GCD COMPUTATION

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Abstract

The problem of finding a *greatest common divisor* (GCD) of any two nonzero polynomials is fundamental to algebraic and symbolic computations, as well as to the decoder implementation for a variety of error-correcting codes. This paper describes new systolic arrays that can lead to efficient VLSI solutions to both the GCD problem and the extended GCD problem.

Comments

Only the Abstract is given here. The full paper appeared as [1]. For the (more difficult) integer GCD problem, see [2, 3].

References

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