

A SYSTOLIC ARRAY FOR THE LINEAR-TIME SOLUTION OF TOEPLITZ SYSTEMS OF EQUATIONS

R. P. BRENT AND F. T. LUK

ABSTRACT

The solution of an $(n + 1) \times (n + 1)$ Toeplitz system of linear equations on a one-dimensional systolic architecture is studied. Our implementation of an algorithm of Bareiss is shown to require only $O(n)$ time and $O(n)$ storage, i.e. constant storage per systolic processor.

COMMENTS

Only the Abstract is given here. The full paper appeared as [1] (there are some unfortunate printer's errors such as omitting parentheses in displayed matrices; the corrections should be self-evident). For related work, see [2]. The numerical stability of the Bareiss and Levinson algorithms (in the symmetric positive-definite case) is considered in [3].

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1991 *Mathematics Subject Classification*. Primary 65Y10; Secondary 47B35, 65Y05, 68Q22, 68Q25, 68Q35.

Key words and phrases. Systolic arrays, Toeplitz matrices, linear equations, Bareiss algorithm, VLSI.

Supported in part by US Army Research Office Grant DAAG 29-79-0124 and in part by the Centre for Mathematical Analysis and the Mathematical Sciences Research Centre at the Australian National University.

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rpb078a typeset using $\mathcal{A}\mathcal{M}\mathcal{S}\text{-}\mathcal{L}\mathcal{T}\mathcal{E}\mathcal{X}$.