

A STABILIZED PARALLEL ALGORITHM FOR DIRECT-FORM RECURSIVE FILTERS

RICHARD P. BRENT AND B. B. ZHOU

ABSTRACT

A stabilized parallel algorithm for direct-form recursive filters is obtained using a new method of derivation in the Z domain. The algorithm is regular and modular, so very efficient VLSI architectures can be constructed to implement it. The degree of parallelism in these implementations can be chosen freely, and is not restricted to be a power of two.

COMMENTS

Only the Abstract is given here. The full paper appeared as [4]. An earlier version appeared as [3]. For related work, see [1, 2].

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COMPUTER SCIENCES LABORATORY, AUSTRALIAN NATIONAL UNIVERSITY, CANBERRA
Current address, Zhou: Department of Radio Engineering, Southeast University, Nanjing, Jiangsu 210018, Peoples Republic of China
E-mail address: {rpb,bing}@cslab.anu.edu.au

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