

PARALLEL ALGORITHMS FOR DIGITAL SIGNAL PROCESSING

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ABSTRACT

This paper provides an introduction to some parallel algorithms relevant to digital signal processing. First we introduce some basic concepts such as *speedup* and *efficiency* of parallel algorithms. We also outline some practical parallel computer architectures – pipelined, SIMD and MIMD machines, hypercubes and systolic arrays.

To illustrate the basic concepts and key issues, we consider the problem of parallel solution of a nonsingular linear system. Gaussian elimination with pivoting is difficult to implement on many parallel architectures, but omission of pivoting leads to numerical instability. A solution is to implement a parallel version of the orthogonal (QR) decomposition instead of the triangular (LU) decomposition obtained by Gaussian elimination. We consider as an application the solution of linear least squares problems, and compare the method of Bojanczyk, Brent and Kung [1] with that of Gentleman and Kung [4].

Many problems in digital signal processing are easy to solve if we can find the singular value decomposition (SVD) of a rectangular matrix, or the eigenvalues and eigenvectors of a symmetric (or Hermitian) matrix. We describe some good parallel algorithms for these problems. Often the parallel algorithms are based on old ideas (such as Jacobi’s method for finding the eigenvalues of a symmetric matrix) rather than on a straightforward adaption of the best serial algorithms. The parallel algorithms can be implemented efficiently on systolic arrays, but we also mention how they might be implemented on other parallel architectures.

COMMENTS

Only the Abstract is given here. The full paper appeared as [2]. Toeplitz systems often arise in digital signal processing, and are considered in a companion paper [3].

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